IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

10 In re Applicant:

Richard L. Coulson et al.

Art Unit:

2863

evial No.:

10/660,310

Examiner:

Aditya S. Bhat

Filed:

September 11, 2003

Docket:

ITL.1029US

P16765

9999999999

For: Adaptive Cache Algorithm for

Temperature Sensitive Memory

Assignee:

Intel Corporation

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed July 13, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

I hereby certify under 30 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class sufficient postage on the date indicated above and is Commissioner for Patents, P.O.